

L Number	Hits	Search Text	DB	Time stamp
7	74	((first near3 (CPU processor)) with (first near3 time near3 period)) and ((second near3 (CPU processor)) with (second near3 time near3 period))	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/07/20 11:55
8	57	(tim\$4 with (allocat\$ assign\$ designat\$4)) with (maximum near3 (packet datagram cell frame) near3 tim\$3)	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/07/20 12:04
9	47514	(memory) same (first with (process\$4 CPU controller)) same (second with (process\$4 CPU controller))	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/07/20 12:14
10	0	((memory) same (first with (process\$4 CPU controller)) same (second with (process\$4 CPU controller))) and (allocat\$ near3 tim\$5) with (maximum near3 (packet cell datagram frame) near3 (process\$3 receive\$3 transmit\$3) near3 tim\$3)	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/07/20 12:17
11	0	((memory) same (first with (process\$4 CPU controller)) same (second with (process\$4 CPU controller))) and (allocat\$ near3 tim\$5) with (maximum near3 (packet cell datagram frame) near3 (process\$3 receive\$3 transmit\$3))	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/07/20 12:18
12	1	((memory) same (first with (process\$4 CPU controller)) same (second with (process\$4 CPU controller))) and (allocat\$ near3 tim\$5) with (maximum near3 (packet cell datagram frame))	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/07/20 12:22
13	203	711/106.ccls.	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/07/20 12:23
14	84	711/106.ccls. and ((memory) same (first with (process\$4 CPU controller)) same (second with (process\$4 CPU controller)))	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/07/20 12:23
15	0	711/106.ccls. and (((memory) same (first with (process\$4 CPU controller)) same (second with (process\$4 CPU controller))) and (allocat\$ near3 tim\$5) with (maximum near3 (packet cell datagram frame)))	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/07/20 12:23
16	6	711/106.ccls. and maximum near3 (packet frame cell datagram)	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/07/20 12:23
-	592	710/33,45,61.ccls.	USPAT; US-PGPUB	2003/04/04 11:57
-	423	710/33,45,61.ccls. and @ad<19990129	USPAT; US-PGPUB	2003/04/03 08:42
-	2341	packet near3 exchange	USPAT; US-PGPUB	2003/04/03 08:42
-	34	(packet near3 exchange) and (process\$3 near3 time) and (DRAM near3 refresh)	USPAT; US-PGPUB	2003/04/03 08:48
-	8347	((multi\$3 near3 processor) or (multi-processor)) and memory and (arbitrat\$ allocat\$3)	USPAT; US-PGPUB	2003/04/03 08:56
-	19	((multi\$3 near3 processor) or (multi-processor)) and memory and (arbitrat\$ allocat\$3)) and (access near3 time near3 allocat\$)	USPAT; US-PGPUB	2003/04/03 09:04
-	93	(access near3 time near3 (allocat\$3 arbitrat\$3)) with (memory storage RAM DRAM)	USPAT; US-PGPUB	2003/04/03 09:06
-	0	4723204.pn. and packet	USPAT; US-PGPUB	2003/04/03 17:08
-	0	4723204.pn. and datagram	USPAT; US-PGPUB	2003/04/03 18:14
-	1	4723204.pn. and block	USPAT; US-PGPUB	2003/04/03 18:14

-	0	((control\$3 near3 (inhibit prevent prohibit) near3 asynchronous) with count\$3)	USPAT; US-PGPUB	2003/04/04 12:40
-	928	metastability	USPAT; US-PGPUB	2003/04/04 12:40
-	0	metastability and (asynchronous with counter)	USPAT; US-PGPUB	2003/04/04 12:40
-	22	metastability and (asynchronous with counter)	USPAT; US-PGPUB	2003/04/04 13:11
-	2160	multiple near3 counter	USPAT; US-PGPUB	2003/04/04 13:12
-	64	(multiple near3 counter) and (time near3 allocat\$3)	USPAT; US-PGPUB	2003/04/04 13:12
-	10867	(packet cell datagram frame) and ((first near3 (processor CPU)) and (second near3 (processor CPU)))	USPAT; US-PGPUB	2003/10/09 11:34
-	13	((packet cell datagram frame) and ((first near3 (processor CPU)) and (second near3 (processor CPU))) and (access near3 time near3 allocat\$3)	USPAT; US-PGPUB	2003/10/09 11:42
-	227	(process\$3 near3 time) with (residual near3 time)	USPAT; US-PGPUB	2003/10/09 14:17
-	369	(process\$3 near3 time) with (residual near3 time)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 11:43
-	498	((first near3 (CPU processor)) with (first near3 time)) and ((second near3 (CPU processor)) with (second near3 time))	USPAT; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 11:48
-	16251	(first near3 time near3 period) and ((second residual near3 time) near3 period)	USPAT; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 12:08
-	39	((first near3 time near3 period) with (access near3 (storage memory))) and ((second near3 time near3 period) with (access near3 (storage memory)))	USPAT; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 12:13
-	39	((first near3 time near3 period) with (first (processor CPU circuit)) with (access near3 (storage memory))) and ((second near3 time near3 period) with (second (processor CPU circuit)) with (access near3 (storage memory)))	USPAT; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 14:24
-	3	((first near3 (processor CPU circuit)) with (packet frame datagram) with (storage memory)) and ((second near3 (processor CPU circuit)) with refresh with (storage memory))	USPAT; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 12:53
-	56	((first near3 (processor CPU circuit)) with (packet frame datagram) with (storage memory)) and refresh with (storage memory)	USPAT; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 13:00
-	345	((packet frame datagram cell) near3 (processor circuit CPU)) same (refresh near3 (memory storage))	USPAT; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 13:03
-	70	((packet frame) near3 (processor circuit CPU)) same (refresh near3 (memory storage))	USPAT; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 13:03
-	6	("4173766" "4631701" "5251176" "5450364" "6011725" "6167484").PN.	USPAT	2003/10/09 14:11
-	0	((process\$3 near3 time) with (residual near3 time)) and (refresh near3 residual)	USPAT; US-PGPUB	2003/10/09 14:18
-	1633	(memory storage DRAM) near3 refresh near3 (circuit)	USPAT; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 14:25

-	28	((memory storage DRAM) near3 refresh near3 (circuit)) and (first near3 time near3 (segment period)) and (second near3 time near3 (segment period))	USPAT; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 14:26
-	6	("4173766" "4631701" "5251176" "5450364" "6011725" "6167484").PN.	USPAT	2003/10/09 14:32
-	386	(refresh near3 (memory storage DRAM)) with (time near3 (period segment))	USPAT; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 15:39
-	23	((refresh near3 (memory storage DRAM)) with (time near3 (period segment))) and (first near3 time near3 (period segment)) and (second near3 time near3 (period segment))	USPAT; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 15:00
-	7	(allocat\$3 with (time near3 (segment period)) with refresh)	USPAT; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 15:01
-	3	refresh with (packet near3 process\$3 near3 time)	USPAT; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 15:43
-	321	refresh with (process\$3 near3 time)	USPAT; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 15:51
-	61	((packet) with (processor CPU circuit) with (DRAM)) and refresh	USPAT; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 15:52
-	1	6338108.pn. and refresh	USPAT; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 16:46